

Q.raxx D101

Digital Measurement Module

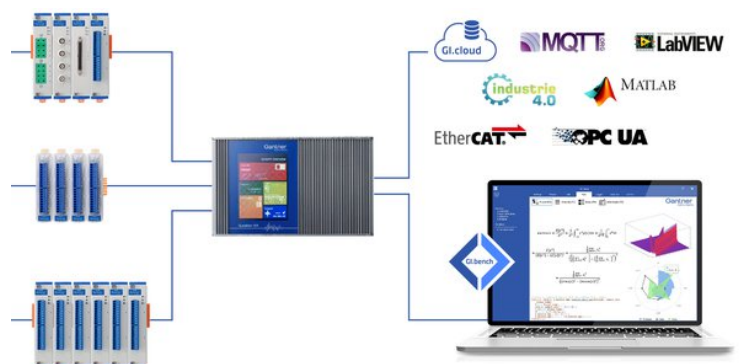
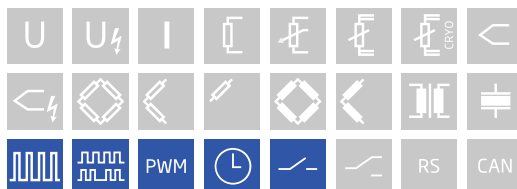
Q.raxx is the ideal 19" rackmount DAQ solution for applications that require high channel density and custom sensor terminations. Q.raxx DAQ systems can utilize an integrated, high-performance controller for communication, control, and data logging purposes. With a controller, multiple Q.raxx systems can be synchronized to each other allowing for efficient DAQ distribution with low jitter and gradual expansion up to thousands of channels.

- High Density
up to 13 I/O modules per Q.raxx 3U chassis with up to 16 channels per I/O module
- User Friendly
front panel indicators for module status, power, and input range error
- Fully Customizable
multiple front panel termination options available
- Maximum Flexibility
parallel communication available in TCP/IP, CAN, PROFIBUS, Modbus, and EtherCAT



Key Features

- 8 digital inputs and 8 digital outputs
configurable as counter, frequency and PWM
- State in and output
process- and host controlled
- Frequency in and output
frequency measurement up to 1 MHz (Chronos method), frequency output up to 10 kHz
- Counter
for/backward counter, quadrature counter with reference zero recognition and missing teeth detection, up to 1 MHz
- PWM in and output
measurement of duty cycle and frequency, output with variable frequency and/or duty cycle
- Galvanic isolation
I/O-signals (4 x 4 I/Os) to power supply and to interface Isolation voltage 500 VDC



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Technical Data

Digital Inputs

Channels	8
Logic levels	TTL or 24 VDC according to IEC 61131-2, Type 1
TTL logic voltage	0 to 0.8 VDC (Low) 2 to 5 VDC (High)
24 VDC logic voltage	-3 to 5 VDC (Low) 11 to 30 VDC (High)
Input type	PNP (current sinking)
Input impedance	>1 k Ω at 0.5 W
Input voltage	30 VDC max.
Input current	2 mA max.
Isolation voltage	500 VDC, group to group, group to power supply, channel to bus ¹

¹ noise pulses up to 1000 VDC, permanent up to 250 VDC

Function Digital Inputs

Status	
Response time	10 μ s
8-fold bit set	specification such as simple state-input, but the binary coded information of 8 inputs can be transmitted as a single variable. This functionality covers all 8 inputs even if they are already used by other functionalities such as counter or frequency measurement. in case of a conflict the Bit-Set is lower prior.
Frequency measurement	
Method	Chronos optimized by combination of the time measurement and pulse counting, recognition of direction of rotation (0 deg./90 deg.)
Frequency range	0.1 Hz to 1 MHz
Time base	0.001 s to 10 s
Internal reference frequency	48 MHz
Resolution	0.002 %
Frequency measurement with recognition of direction of rotation	specification like frequency measurement, for the recognition of the rotation direction the phasing of both inputs is being used
Pulse counting	
Resolution	32-bit (± 31 -bit)
Counter frequency	1 Mhz
Forward and reverse counting	with an additional input for the direction of counting
Quadrature counter	with an additional input for the direction recognition for phasing the inputs
Quadrature counter with zero reference and reset/enable	like quadrature counter but with two additional inputs for the 0-reference recognition and enabling the 0-reference recognition
PWM measurement	
Input frequency	0.1 Hz to 1 MHz
Resolution	21 ns
Configuration of the measurement type	Pulse counting for duty cycle, frequency

With a D101 - 2 x 4 terminals for digital inputs are available. Those will accept all mentioned signals as it required. The following combinations are possible.

Connector 1				Connector 2			
Terminal 1.6	Terminal 1.7	Terminal 1.8	Terminal 1.9	Terminal 2.6	Terminal 2.7	Terminal 2.8	Terminal 2.9
Status	Status	Status	Status	Status	Status	Status	Status
Status	Status	Status	Status	Status	Status	2 channel signal ¹	
Status	Status	Status	Status	2 channel signal ¹		2 channel signal ¹	
Status	Status	Status	Status	4 channel signal ²			
Status	Status	2 channel signal ¹		2 channel signal ¹		2 channel signal ¹	
Status	Status	2 channel signal ¹		4 channel signal ²			
2 channel signal ¹		2 channel signal ¹		4 channel signal ²			
2 channel signal ¹		2 channel signal ¹		2 channel signal ¹		2 channel signal ¹	
2 channel signal ¹		2 channel signal ¹		4 channel signal ²			
4 channel signal ²				4 channel signal ²			

¹ All digital functionalities except status and quadrature counter with zero reference and reset/enable

² Quadrature counter with zero reference and reset/enable

Digital Outputs

Channels	8
Output voltage	10 V to 30 VDC
Load capacity	30 VDC / 500 mA (ohmic load)
Contact	open drain p-channel MOSFET

Function Digital Outputs

Status			
Response time (depending on load capacity)	>0.5 A 10 µs	>0.1 A 100 µs	<0.1 A 1000 µs
8-fold bit set	specification such as simple state-output, but the binary coded information of 8 outputs can be transmitted as a single variable. This functionality covers all 8 outputs even if they are already used by other functionalities such as counter or frequency measurement. in case of a conflict the Bit-Set is lower prior.		
Frequency output			
Frequency range	0.1 Hz to 1 kHz / 10 kHz depending on load capacity		
Accuracy	0.1 %		
Resolution	1 µs		
PWM output			
Frequency range	0.1 Hz to 1 kHz / 10 kHz depending on load capacity		
Accuracy	0.1 %		
Resolution	1 µs		

Communication Interface

Electrical standard	RS-485, 2-wire
Protocols	local bus: 115200 bps to 48 Mbps, Format 8e1, Modbus RTU, ASCII: 19200 bps to 115200 bps

Power Supply

Input voltage	10 to 30 VDC, overvoltage and overcurrent protection
Power consumption	approx. 2 W
Input voltage influence	<0.001 %/V

Environmental

Operating temperature	-20°C to +60°C
Storage temperature	-40°C to +85°C
Relative humidity	5 % to 95 % at 50°C, non-condensing

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Remarks

Warm-up time	validity of all listed specifications are subject to a warm-up period of at least 45 minutes
	specifications subject to change without notice

Ordering Information

Article number	102116
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